



APPENDIX A
"CLEAN" VERSION OF EACH PARAGRAPH/SECTION/CLAIM
37 C.F.R. § 1.121(b)(ii) AND (c)(i)

SPECIFICATION:

Replacement for the paragraph beginning at page 7 beginning at line 3:

Said video processor 3 is composed of a DSP for video signal processing 21 (DSP stands for digital signal processor) for driving said CCD 16 or generating a video signal, a drive amplifier 22 for amplifying a drive signal, which is output from the DSP for video signal processing 21 to drive said CCD 16, a preamplifier 23 for amplifying an imaging signal transmitted from said CCD 16, a CDS circuit 24 for performing CDS (Correlated Double Sampling) processing on the imaging signal output from the preamplifier 23 to extract a video signal component, an A/D converter circuit 25 for converting the video signal obtained in the CDS circuit 24 to a digital signal to give it to said DSP for video signal processing 21, a control microprocessor 26 for controlling said DSP for video signal processing 21 and each part of the video processor 3, a ROM 27 for storing software that the control microprocessor 26 executes, and a set switch 28 for a DIP switch and the like which can detect set condition through said control micro processor 26.

CLAIMS (with indication of amended or new):

9. (Amended) An endoscope apparatus according to claim 7:

wherein information from which said delay time can be derived includes identification information for identifying a type of said endoscope.

10. (Amended) An endoscope apparatus according to claim 5, comprising:

 said endoscope including information acknowledgement portion for giving information indicating said delay time to said second processor; and

 said second processor setting said delay time depending on information acknowledged from said information acknowledgement portion.

11. (Amended) An endoscope apparatus according to claim 5, comprising:
said endoscope including a information acknowledgement portion for giving information
from which said delay time can be derived to said second processor; and
said second processor setting said delay time depending on information acknowledged
from said information acknowledgement portion.
